

## **DETAILED ACTION**

### ***Continued Examination Under 37 CFR 1.114***

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 09/18/2009 has been entered.

### **EXAMINER'S AMENDMENT**

2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Michael Martensen on 11/30/2009.

The application has been amended as follows:

**Claim 7 depend on claim 1.**

***Allowable Subject Matter***

3. The following is an examiner's statement of reasons for allowance:

Claims 1-3, 5-12 and 14-21 are allowable.

With regard to claims 1, 10 and 19, the closest prior art of record Bolan et al. (US 5,684,828) teaches

producing a control pulse after having received a certain number of chips of the received signal ,

controlling a variable delay applied to the received signal ; and

Ishigaki et al. (US 4,977,578) teaches

sending, to demodulation units in the receiver, a delayed signal in which chips have been omitted or duplicated on the basis of said control pulse ,

supplying, to said demodulation units in the receiver, a compensation signal that indicates whether chips have been omitted or duplicated in the delayed signal ;

However, Bolan or Ishigaki alone or in combination fails to teach or fairly suggest

receiving a signal comprising both data chips and pilot chips,

demodulating in a first and a second demodulation unit the delayed signal such that the demodulation units consider the omission or duplication of chips in the delayed signal and demodulating the received pilot chips in the first demodulation unit to produce demodulated pilot chips and the received data chips in the second demodulation unit to produce demodulated data chips.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to KIET DOAN whose telephone number is (571)272-7863. The examiner can normally be reached on 8am - 5pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Charles N. Appiah can be reached on 571-272-7904. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Kiet Doan/  
Examiner, Art Unit 2617

/Charles N. Appiah/  
Supervisory Patent Examiner, Art Unit 2617